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... **optical** and process **simulation** are **Optical Rule Checking** ... image (Figure 4). Conventional DRC **rules** implement these ... A fast **ORC check** then determines which edges ...[View as HTML](#) - [Web Search](#) - [nsti.org](#) - [comppub.com](#) - [nsti.org](#)Sub-Wavelength Lithography Using OPC

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... An **ORC check** can describe the magnitude of the problem ... be more broadly used to represent "**optical** and process ... form of simple shape manipu- lation **rules**, or a ...[Web Search](#) - [semiconductorfabtech.com](#) - [eecs.umich.edu](#) - [semiconductorfabtech.com](#)A Fast Lithography Verification Framework for Litho-Friendly Layout Design

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... To **check** the post-OPC mask data, the litho ... in Figure 6. Here, is the **optical** system wavelength ... OPC works within the constraints of mask manufacturability **rules**. ...[Web Search](#)Adoption of OPC and the Impact on Design and Layout

FM Schellenberg, O Touban, L Capodici, B Socha - DAC, 2001 - portal.acm.org

... by changing aperture sizes and shapes (**optical** and process ... designed layout and a DRC **check** of the ... generated, and then checked using conventional design **rules**. ...[Cited by 10](#) - [Web Search](#) - [sigda.org](#) - [hwswworld.com](#) - [videos.dac.com](#) - [all 12 versions »](#)Detailed Placement for Improved Depth of Focus and CD Control

P Gupta, AB Kahng, CH Park - Proc. Asia and South Pacific Design Automation Conf., Jan, 2005 - vlsicad.ucsd.edu

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A Reich, R Boone, W Grobman, C Browning - 21 stAnnual BACUS Symposium on Photomask Technology, ..., 2001 - io.com

... different range effects and yield different physical and **optical** responses, so ... Delay Models Topology / **Rules** ... crucial **check** in the case of tiling is to assure ...[Cited by 2](#) - [View as HTML](#) - [Web Search](#) - [si2.org](#) - [adsabs.harvard.edu](#) - [link.aip.org](#) - [all 5 versions »](#)Estimating the spectral response function of the CASI-2

KY CHOI, EJ MILTON - eprints.soton.ac.uk

... is reasonable, as there are general **rules** that approximately ... the detector array within

the same **optical** system (pers. ... providing a simple way to **check** for any ...

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Quantum computation and Shor's factoring algorithm

A Ekert, R Jozsa - Reviews of Modern Physics, 1996 - [link.aps.org](#)

... It followed by a trial division to **check** whether m ... all the amplitudes using the transition **rules** of the ... than the time scale of the **optical** interaction (the ...

Cited by 477 - [Web Search](#) - [hep.princeton.edu](#) - [www-tfp.physik.uni-karlsruhe.de](#) - [pi1.physi.uni-heidelberg.de](#) - [all 6 versions »](#)

Ab initio calculations of the lattice dynamics of boron nitride nanotubes

L Wirtz, A Rubio, RA de la Concha, A Loiseau - Physical Review B, 2003 - [link.aps.org](#)

... Therefore, as a first **check** on the frequencies of active ... groups have been used to evaluate the section **rules** for the ... 7~a!# which are related to the **optical** out-of ...

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CA Mack - FINLE Technologies (Austin, TX: 1997)

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Title: Three-dimensional simulation of optical lithography Authors: Toh,

Kenny K.; Neureuther, Andrew R. Affiliation: AA(Univ. of ...)

[Cited by 8](#) - [Web Search](#)**Efficient and rigorous three-dimensional model for optical lithography simulation**KD Lucas, H Tanabe, AJ Strojwas - J. Opt. Soc. Am. A, 1996 - [josaa.osa.org](#)

... for optical lithography simulation ... 6,7 This new model is ideally suited to the

simulation of 3-D optical lithography processing problems. ...

[Cited by 4](#) - [Web Search](#) - [josaadev.osa.org](#) - [josaa.osa.org](#) - [adsabs.harvard.edu](#)**Impact of lens aberrations on optical lithography**T Brunner - IBM Journal of Research and Development, 1997 - [research.ibm.com](#)... simulated by the PROLITH/2* simulation program² under ... three-leaf-clover aberration

on lithography is to ... a few nm precision by modern optical overlay metrology ...

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... This enhances depth of focus while maintaining high resolution capability in optical

lithography. ... Simulation and modeling of fabrication processes.

[Cited by 11](#) - [Web Search](#) - [adsabs.harvard.edu](#)**Resolution Enhancement Techniques in Optical Lithography**K Ronse, L Van - [fabtech.org](#)

... Figure 2 Lumped parameter model simulation illustrating the improvement in DOF ... Figure

4 (above) The optical lithography roadmap, detailed with the various ...

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... in a half-micron optical lithography by the focus latitude enhancement exposure method:

Simulation ...H Fukuda, N Hasegawa, S Okazaki - Journal of Vacuum Science & Technology B: Microelectronics ..., 1989 - [dx.doi.org](#)

>/> Improvement of defocus tolerance in a half-micron optical lithography by the focus latitude enhancement exposure method: Simulation and experiment. ...

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Authors: Arthur, Graham G.; Wallace, Christine; Martin, Brian Affiliation: AA ...

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Simulation of optical lithography

A Erdmann, W Henke - Proceedings of SPIE, 2003 - adsabs.harvard.edu

Title: **Simulation of optical lithography** Authors: Erdmann, Andreas; Henke, Wolfgang

Affiliation: AA(Fraunhofer Institute for Silicon Technology) Journal: Proc. ...

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C Mack - Inc.(1997)

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AB Kahng, YC Pati - ISPD, 1999 - [portal.acm.org](#)

... essential com- panion to the "**checking-centric**" mindset ... the context of subwavelength

optical lithography, the above ... and PSM-related design **rules** will move ...

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Resolution Enhancement Techniques in **Optical Lithography**, It's not just a Mask Problem

LW Liebmann - ... and Next-Generation **Lithography** Mask Technology VIII, Proc. ... - [eecs.umich.edu](#)

... Absent from Table 1 is **optical** proximity correction, customarily ... have to be optimized

experimentally or through **simulation** by the **lithography** groups and ...

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... Phase Shifted Mask Designs for a Full Logic Gate Level: Design **Rules** and Design Rule **Checking**

L Liebmann, J Lund, FL Heng, I Graur - DAC, 2001 - [portal.acm.org](#)

... the lifetime of a given generation of **optical lithography** tools. ... **rules** are derived

from the **lithography** system in ... One could use a **simulation**-based method which ...

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Practical applications of 2-D **optical** proximity corrections for enhanced performance of 0.25 μ m ...

H Chuang, P Gilbert, W Grobman, M Kling, K Lucas, ... - Electron Devices Meeting, 1997. Technical Digest., ..., 1997 - [ieeexplore.ieee.org](#)

... an interactive cycle involving **optical** modeling, OPC ... could be achieved using these

tuned **lithography** simulations in ... Accurate tuning of the **simulation** models is ...

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Impact of RET on physical layouts

FM Schellenberg, L Capodiecici - ISPD, 2001 - [portal.acm.org](#)

... concerns techniques known as OPC (**Optical** & Process ... is a physical modification to

the **lithography** tool, and ... understood, and can be modeled through **simulation**. ...

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Model-based OPC/DRC considering local flare effects

H Futatsuya, T Yao, M Osawa, K Ogino, H Hoshino, H ... - Proceedings of SPIE, 2004 - [link.aip.org](#)

... manufacturing IC devices that use **lithography** generations of ... simply by the sum of

optical intensity and ... impact of local flare, the **simulation** model including ...

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... Both **rules**- based corrections, for simple improvements, and model ... example, all papers

in Session 5: **Lithography Simulation**, in **Optical/Laser Microlithography** ...

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Silicon-Level Physical Verification of Sub Wavelength (tm) Designs

FC Chang, M Kwok, K Rachlin, R Pack, NT Inc, CA ... - Proceedings of the 12th International Conference on VLSI ..., 1999 - [doi.ieeecomputersociety.org](#)

... with a set of manufacturing process **rules** such as the ... this by using very fast

lithography simulation to produce ... using Practical 2-D **Optical** Proximity Correction ...

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A Fast Lithography Verification Framework for Litho-Friendly Layout Design

YC Ban, SH Choi, KH Lee, DH Kim - [doi.ieeecomputersociety.org](#)

... Kim, M. Yoo, J. Kong, "**Simulation**-based critical ... friendly layout design for low **lithography**", Proc ... Resolution enhancement techniques in **optical lithography**, 2001 ...

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Optical proximity correction (OPC): friendly maze routing

MDF Wong - Proceedings of the 41st annual conference on Design ..., 2004 - [portal.acm.org](#)

... The distortions in opti- cal **lithography** include corner ... The design **rules** basically capture the first order ... is different from the **optical simulation** grid size. ...

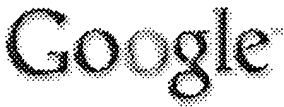
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This has sometimes been called an **ORC**, for **Optical Rules Check**, ... The resulting field dependent aberrations can be used in the **simulation** algorithms that ...

www.future-fab.com/documents.asp?grID=213&d_ID=1202 - 92k - [Cached](#) - [Similar pages](#)

Optimal insertion points for OPC and PSM in design flows

... the introduction of the "OPC DRC" step (more commonly called **ORC**, **optical rules check**) ensures that OPC changes are actually manufacturable, ...

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Calibre **ORC** (**Optical** and Process. Rule Checking) provides full-chip ... DRC".

The **simulation** is based on the. **optical** model and process model ...

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generated by CMP **simulation**. The wafer topography variations result in local ...

OPC and **optical** rule checking (**ORC**). TOPC is performed based on the DMLs. ...

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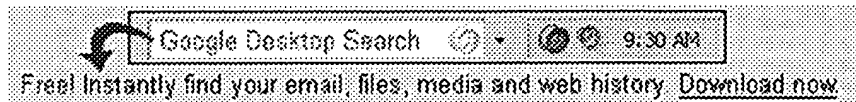
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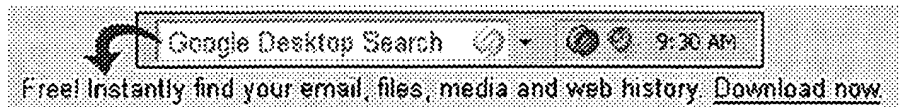
UETA and the subsequent administrative **rule** apply only to electronic records ...

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... individual **layout** blocks, both using **DRC** and **LVS**, the purpose of running **LVS**
now is ... This can cause design **rule violations** in the top-level **layout**, ...
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the **DIVA DRC** and also **LVS violations**. The **DIVA rules** file replicates the ...
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... **LVS**. Make sure your **layout** design is **DRC** clean; Save all cellviews. ...
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Intelligent **layout** automation in CosmosLE results in **LVS**- and **DRC**correct cell and
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HJTC's Customer Design Support Department (CDS), provides **DRC**, **LVS** and **LPE** ...
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LVS (layout versus Schematic) is another important verification. While DRC just
checks if your layout follows the rules set by a technology, ...
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Frequently when I'm reviewing a clients SQL Server **database design**, the in-house ... We have a program called SSW SQL Auditor to **check** for this **rule**: ...
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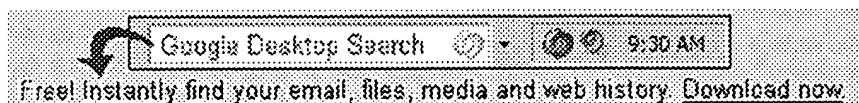
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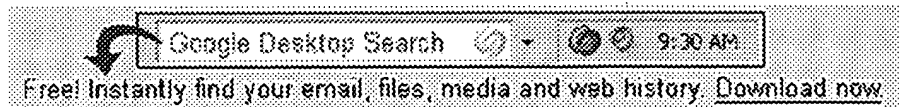
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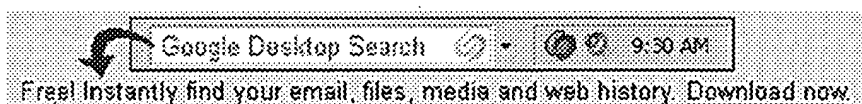
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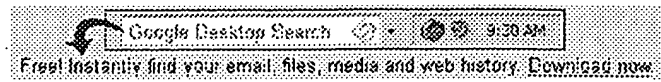
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